Series FVC1

SINEWAVE HF VCXO

Description

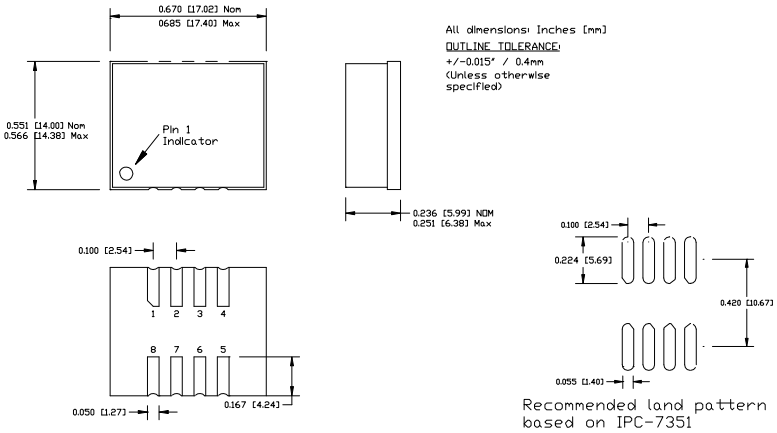
The FVC1 Series of voltage controlled crystal oscillators (VCXO) provides high frequency with Sine-Wave output. The device does not use any frequency multiplication, providing exceptionally low Phase Noise and Jitter. It's packaged in a miniature, FR-4 based 17x14 mm SMD package.

Applications and Features

- Fiber Channel; 10 GbE; Infiniband; Network Processors; SONET/SDH
- High Reliability – qualified for crystal oscillator start-up conditions
- Extremely Low Phase Noise and Jitter
- No Multiplication
- Absolute Pull Range (APR) to ±1,000 ppm
- SONET ±20 ppm overall free-run stability available
- High Shock Resistance, to 1000g
- Militray/Space

Part Numbering Example: FVC1ADFM- XX.XXXX M

FVC1	A	D	F	M	XX.XXXXM
SERIES	VOLTAGE	OPERATING TEMP.	ABSOLUTE PULL	GRADE	FREQUENCY
	0= 5.0V A= 3.3V	A = 0 ℃ to 50 ℃ B = -20 ℃ to 70 ℃ C = -30 ℃ to 70 ℃ D = -40 ℃ to 85 ℃ CS=Customer Specific	H= ±100 ppm G= ± 50 ppm F= ± 32 ppm E= ± 20 ppm D= ± 10 ppm	N = Industrial Std M= MIL-STD-883B B=MIL-PFR-55310D	12 ~250.000 MHz



OUTLINE TOLERANCE:
±0.015" / 0.4mm
(Unless otherwise specified)

PIN FUNCTIONS:
[1] Vcc
[2] [3] [4] [7] - GND
[5] OUTPUT
[6] N/C
[8] Vc

Environmental and Mechanical Characteristics

Operating temp. range	see part # table
Mechanical Shock	Per MIL-STD-202, Method 213, Cond. A
Thermal Shock	Per MIL-STD-883, Method 1011, Cond. A
Vibration	Per MIL-STD-883, Method 2007, Cond. A
Hermetic Seal	Leak rate less than $5x10^{-8}$ atm.cc/s of helium , crystal only.
Soldering conditions	See MAX reflow profile below; The device may be reflowed once. Reflowing upside down is not allowed. NO CLEAN assembly is recommended.

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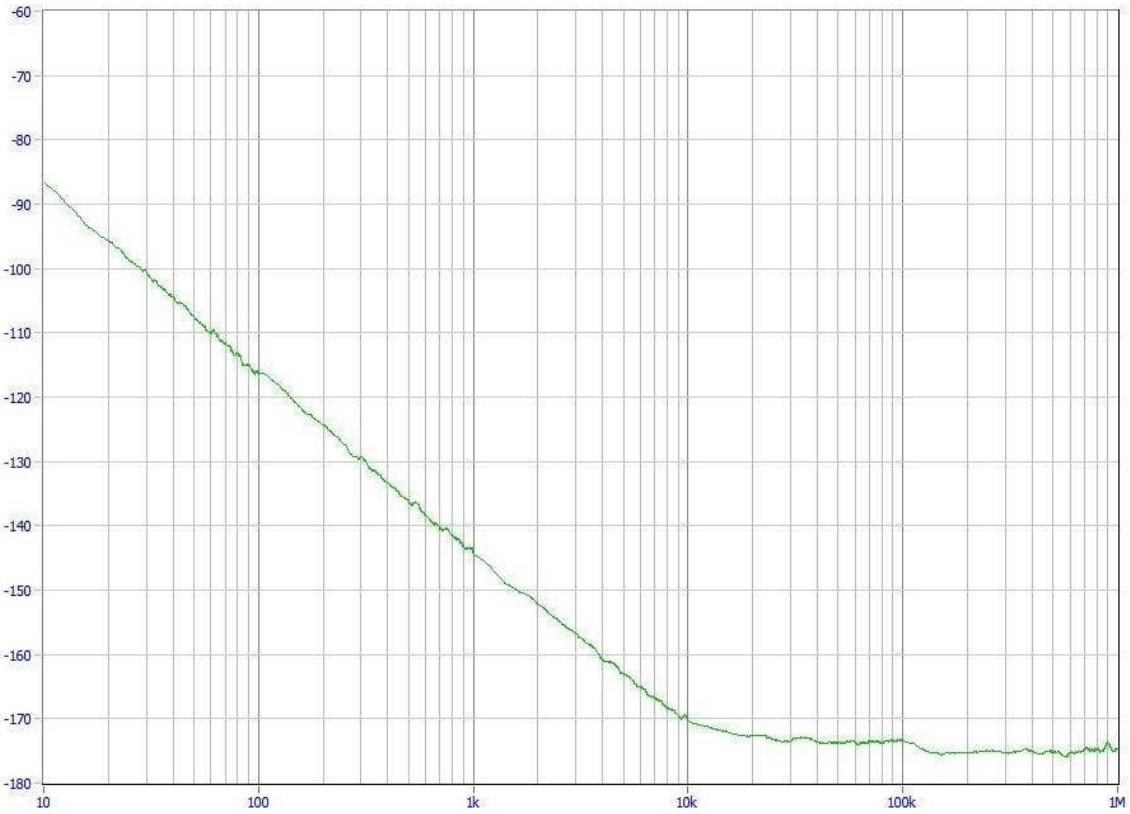
Electrical Parameters

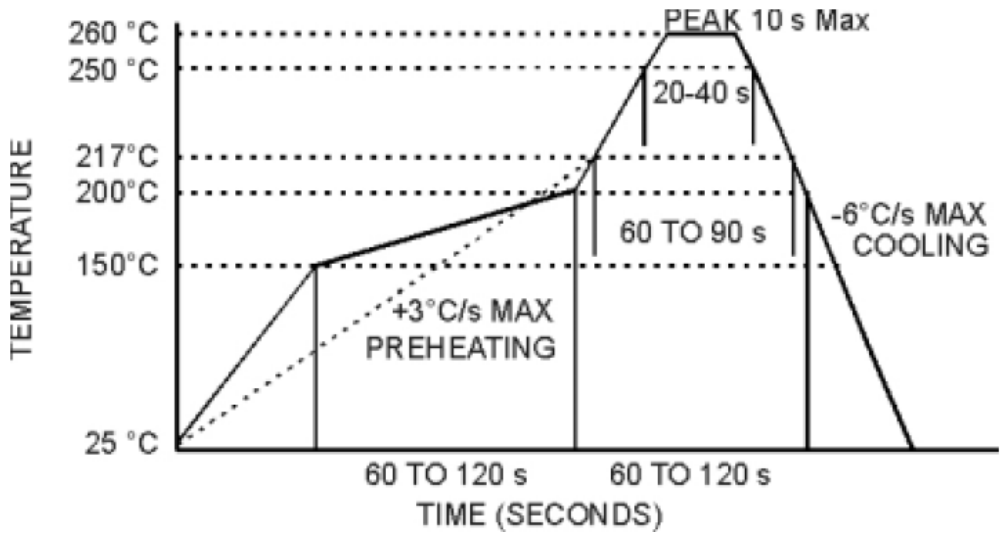
Parameter		Symb	Conditions, Note		MIN	TYP	MAX	Unit
Nominal Frequency		Fo	See Note below		12		250	MHz
Supply Voltage		Vcc	Code 0 Code A		4.75 3.135	5.0 3.3	5.25 3.465	V
Supply current		Icc	No load, Vcc=3.3V 100MHz			60	160	mA
Output Logic Type						Sine		
Load			Internally AC coupled		45	50	55	Ohm
Harmonic		Ph					-25	dBc
Sub-Harmonics						None		
Output Power		Po	Into 50 ohm,5V 3.3V		7 5	10 7		dBm
Jitter	Integrated, RMS	J	Integrated from Phase Noise, 12 KHz to 20 MHz RMS			0.1	0.15	ps
			100Hz to 80KHz,RMS				0.5	ps
			50 KHz to 80 MHz			0.2		ps
	Wavecrest characterized		Random period,			2.5		ps
			Accumul., pk-to-pk			17		ps
			Determin.			0		ps
Phase Noise		£(Δf)	100 MHz, 3.3V APR 32 ppm or less	@ 10 Hz @100 Hz @1 KHz @10KHz @100KHz @>1MHz		-85 -115 -145 -168 -171 -172	-80 -110 -140 -166 -169 -170	dBc/Hz
Frequency Stability, usually not specified – unless necessary, APR is specified to incorporate stability		ΔF/F	Overall, including temperature, aging 10 years, shock and vibration @Vc=Vcc/2; APR 50 ppm, or less		±20	±30		ppm
Control Voltage Range		Vc			0V		Vcc	V
Setability		Vcs	Vc to set the F at Fo; T, Vcc, load – nominal, as shipped		0.4 Vcc	0.5 Vcc	0.6 Vcc	V
Absolute Pull Range		APR	Over all conditions, see part # creation		10, 20, 32, 50, 100			ppm
Input impedance		Zin	@ Fmod < 100 KHz		50			KOhm
Modulation Bandwidth			At Vc = Vcc/2, -3dB		20			KHz

Parameter	Symbol	Value	Unit
Operating Temperature Range	To	-40 to +85	℃
Storage Temperature Range	Tst	-50 to +90	℃
Supply Voltage	Vcc	-0.5 to 5.5	V
Control Voltage	Vc	-0.5 to 5.5	V

Typical Phase Noise at 100 MHz



MAX Reflow Profile



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